Sameer Pendharkar’s innovative leadership and technical contributions have resulted in TI consistently achieving the best in-class, cost efficient and smallest power devices in the world over multiple power technology generations.

Sameer joined Texas Instruments Incorporated (TI) in 1996 and is currently a TI Senior Fellow and the Analog Technology Roadmap Manager inside the company’s Analog Technology Development. He and his team are responsible for setting and executing the overall analog technology development strategy for TI.

Over the years, Sameer has been instrumental in enabling TI to enter and compete in the high voltage market space through the definition and development of TI-first high voltage silicon and gallium nitride technologies. He has published over 90 technical papers in the broad area of power and high voltage technology in leading academic journals and conferences and has been granted more than 170 U.S. patents.

As a member of the IEEE-EDS Power Devices and Integrated Circuit Committee, Sameer helped co-create a first-of-its-kind, yearlong, eight session webinar course given by industry experts and university faculty. This was available for all IEEE members across the nation to help increase the interest of young engineers in the field of power electronics.

He also gave short courses and tutorial presentations on power device technology and modeling at the University of Texas at Dallas, Indian Institute of Technology Mumbai and IEEE-EDS Orlando Chapter (University of Central Florida).

For his work on power device architecture and technology, Sameer was awarded the prestigious Edith and Peter O’Donnell Award for Technology Innovation by The Academy of Medicine and Science of Texas (TAMEST).